

## 5G Lab Partnership of Racyics - Press Release

### Racyics announces cooperation with the 5G Lab Germany

Dresden, GERMANY – July, 2017 – Racyics GmbH announces a cooperation with the 5G Lab Germany ([www.5glab.de](http://www.5glab.de)). In cooperation with the chair of Highly Parallel VLSI Systems and Neuro Microelectronics in the 5G lab Germany, Racyics develops advanced Mixed-Signal Circuits, such as analog-to-digital converters for analog frontends, for IoT applications in the most advanced nodes like GLOBALFOUNDRIES' newest 22nm FDSOI technology 22FDX®. Cooperation is based on makeChip ([www.makechip.design](http://www.makechip.design)) an innovative hosted design service platform developed by Racyics in close collaboration with GLOBALFOUNDRIES. Targeted for start-ups, SMEs, research institutes and universities, makeChip is a central gateway to design integrated circuits based on advanced semiconductor technologies.

The platform provides IT infrastructure with a full set of EDA tool installations and technology data setup, i.e. PDKs, foundation IP, complex IP. All tools and design data are linked by Racyics' silicon proven design flow and project management system. The turnkey environment enables any makeChip customer to realize complex System on Chips in the most advanced technology nodes.

"The cooperation with Racyics enables us to drive our research towards the development of 5G applications in leading edge technologies" stated Prof. Christian Mayr, Head of the chair of Highly Parallel VLSI Systems and Neuro Microelectronics.

As industry partner of the 5G Lab Germany Racyics will bring in its experience in complex SoC realizations and optimize *makeChip* to 5G requirements. This will enable and speed up the implementation of future 5G systems.

"In cooperation with the 5G lab Racyics drives the development of a design and IP ecosystem which enables innovative research groups and start-up companies to realize extremely energy efficient SoCs with integrated mixed signal and RF components for 5G applications based on GLOBALFOUNDRIES' 22FDX®", stated Holger Eisenreich, CEO of Racyics.

"Racyics' platform enables 5G Lab Germany members and partners to accelerate prototyping their innovative hardware solutions in most advanced semiconductor technologies. This is an essential paving stone on our road towards 5G." stated Prof. Gerhard Fettweis, 5G Lab Germany.

For more information, please go to [www.racyics.de](http://www.racyics.de) and [www.5glab.de](http://www.5glab.de) .

### About Racyics

Racyics® is an experienced design house based in Dresden, Germany. We offer design and implementation services for analog, mixed-signal and digital ICs. Working for leading European semiconductor companies for many years, the Racyics team contributed to numerous successful chip designs down to 28nm feature size for automotive, consumer and communication applications. As GLOBALFOUNDRIES' channel partner with focus on advanced and leading edge technologies, Racyics provides access to 28nm, 22nm and 14nm prototyping runs (MPWs). Furthermore, Racyics offers design enablement services for European SMEs and academia.

## **About 5G Lab Germany**

Within the 5G Lab Germany, twenty-two professors from TU Dresden collaborate in an interdisciplinary team with more than 600 scientists to advance research on the key technologies for the 5<sup>th</sup> generation of mobile communications (5G) and its applications. A key feature of 5G will be a short latency that will enable Tactile Internet applications, e.g. automated driving, robotic-aided tele-surgery, as well as new learning and trainings methods with special tactile-to-visual feedback. To achieve this goal, the researchers in the 5G Lab Germany are addressing the whole value chain: from the semiconductor chips across wireless data transmission, networking and mobile edge clouds to Tactile Internet applications.